

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: :  
:  
Averett, Guy E. et al. : Examining Group: 2811  
:  
Serial No.: 10/072,145 :  
Filed: 02/07/2002 : Examiner: Ori Nadav

For: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS  
OF LOW SUBSTRATE CAPACITANCE

**RESPONSE UNDER 37 CFR 1.116 EXPEDITED PROCEDURE**

EXAMINING GROUP 2811

Honorable Commissioner of Patents,  
P.O. BOX 1450  
Alexandria, VA 22313-1450

SIR:

In response to the Office Action mailed October 20, 2009, please reconsider the above indentified application in view of the amendments and remarks presented hereinafter.